automata-theoretic model checking

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(starting next week, my affiliation will be U. of Edinburgh)
The purpose of my lectures:
- to cover the fundamental algorithms used in the automata-theoretic approach to (LTL) model checking, as practiced in tools such as SPIN.

In doing so, we must cover key models and formalism: transition systems, Büchi automata, (linear) temporal logic, .........

We will only briefly touch on other approaches and more advanced algorithms.

Apology: I won’t provide thorough references to original research. Please see, e.g., references in the book [CGP’00].
basic picture of model checking

- system model $M$
- property $\psi$

model checker

(Does $M \models \psi$?)

Yes!

No! + “counterexample”
where do we get the system model?

**hardware**
- e.g., Verilog or VHDL, source code

**software**
- e.g., C, C++, or Java, source code
- hand-built design models

**state machine-based system model**

abstraction & other (semi-)automated transformations
where do we get the properties?

- requirements documentation
- (insight)
- canned standard properties & templates (e.g., “deadlock-freedom”)

formal properties (typically based on temporal logic or automata)
choosing a system model: what do we want to model?

- systems have a **state** that evolves over time.
- they manipulate data, accessed through **variables**, whose values change as the state changes.
- **non-determinism** does not exist in real systems, but it lets us model unknowns of the system and its environment, allowing many possible evolutions of system state.
- **concurrency**: systems have interacting **processes**
  - asynchronous/synchronous,
  - message passing or shared data communication.
- possibly: dynamic memory allocation & process creation, procedure call stack, clocks and real time, etc., etc.,
  ----------------
- **wait! don’t get carried away....**
we need models to be:

- **amenable to efficient algorithmic analysis**, 
- yet to faithfully model as many relevant aspects of real systems as possible.
Example: simple Promela implementation of a semaphore
(Promela is the input language of the SPIN model checker)

```
chan semaphore = [0] of { bit };
proctype dijkstra() {
    byte count = 1;
    loop:
        if
           :: (count == 1) → semaphore!P;  count = 0 ; goto loop
           :: (count == 0) → semaphore?V;  count = 1 ; goto loop
        fi
    }
proctype user() {
    repeat:
        semaphore?P; /* put critical section here .... */
        semaphore!V; /* put non-critical section here .....*/
        goto repeat
    }
init { run dijkstra();  run user();  run user() }
```
An **EFSM** is given by $\mathcal{M} = (Q, V, \text{Guards}, \text{Actions}, \Delta, \text{init})$:

- $Q$ is a finite set of **control states**.
- $V = \{v_1, v_2, v_3, \ldots, v_m\}$ is a set of **variables**. Each $v_i \in V$ takes values from a finite domain $D(v_i)$.
- $\text{Guards} = \{grd_1, grd_2, \ldots, grd_r\}$, are a set of predicates $grd_j(v_1, \ldots, v_m)$, that evaluate to true or false given an assignment $\alpha \in D(v_1) \times D(v_2) \times \ldots \times D(v_m)$ to the variables.
  - guard example: “$v_2 \geq v_4$”.
- $\text{Actions} = \{\text{act}_1, \text{act}_2, \ldots, \text{act}_d\}$ are a set of functions $\text{act}_j : D(v_1) \times \ldots \times D(v_m) \mapsto D(v_1) \times \ldots \times D(v_m)$ that given one assignment to the variables, produce another assignment.
  - action example: “$v_3 := v_1 + v_2 + 1$”.
- $\Delta \subseteq Q \times \text{Guards} \times \text{Actions} \times Q$ is a set of **transitions**.
- init = $(q_{\text{init}}, \alpha_{\text{init}})$ is an **initial** control state $q_{\text{init}} \in Q$ together with an initial assignment $\alpha_{\text{init}}$ to the variables.
justifying our choice

What happened to concurrency?

- No problem. If we have processes $\text{Proc}_1, \ldots, \text{Proc}_n$, with sets of control states $Q_1, \ldots, Q_n$, respectively, let the product $Q_1 \times \ldots \times Q_n$ be our set of control states $Q$ of $\mathcal{M}$.

- In asynchronous concurrency, the transition relation $\Delta$ would modify the control state of only one process per transition.

- In synchronous concurrency, all process states could be modified in the same transition.

- Bounded message buffers ("channels" in Promela) can be modeled by variables.
Doesn’t this hide the state explosion of the product $Q_1 \times \ldots \times Q_n$?

• Not really. Variables already yield state explosion. In fact, we can eliminate control states entirely, encoding them as variables: let variable $pc_i$ encode the “program counter” value for process $Proc_i$. Transitions of $\mathcal{M}$ are then just pairs in Guards $\times$ Actions, that update program counters appropriately.
What about dynamic process creation and dynamic memory allocation?

- In principle, EFSMs are finite state and do not model systems whose state space can be unbounded.

But tools like SPIN allow dynamic process creation, don’t they?

- Yes, but no problem. The algorithms we describe for EFSMs apply to a more general model, where the state space could be infinite, but without any guarantee of halting.

- To keep things simple, we confine our descriptions to EFSMs. This already conveys the essence of all the algorithms.

- But we will soon describe more general assumptions, sufficient for the algorithms to be applicable.

What about procedure calls? What about clocks and real time?

- See J. Esparza’s and K. Larsen’s lectures, respectively.
The underlying transition system of an EFSM

- An EFSM, $\mathcal{M}$, provides a concise representation of an underlying state transition system $\mathcal{K} = (S, R, \text{init})$:
  
  - $S = Q \times (D(v_1) \times \ldots \times D(v_m))$ is the set of (full) states of $\mathcal{K}$, given by control state + data values in $\mathcal{M}$.
  
  - $R \subseteq S \times S$, the transitions of $\mathcal{K}$, are:
    
    for all states $s = (q, \alpha) \in S$ and $s' = (q', \alpha') \in S$,
    
    $(s, s') \in R$ iff $\exists (q, \text{grd}, \text{act}, q') \in \Delta$ such that
    
    $\text{grd}(\alpha) = \text{true}$ and $\text{act}(\alpha) = \alpha'$.

  - $\text{init} = (q_{\text{init}}, \alpha_{\text{init}}) \in S$, is also the initial state of $\mathcal{K}$.

- Each execution of $\mathcal{M}$ defines an (infinite) path $\pi$, called a run through states in $\mathcal{K}$, where $\pi = s_0 \rightarrow s_1 \rightarrow s_2 \rightarrow \ldots$ such that $s_0 = \text{init}$, and $(s_i, s_{i+1}) \in R$, $\forall i \geq 0$.  

labeling the transition system

• Suppose we’re interested in certain “observations” or “propositions” \( P = \{ p_1, \ldots, p_k \} \) regarding states of \( \mathcal{K} \). Each

\[
p_i: S \mapsto \{\text{true, false}\}
\]

is a predicate on states

(i.e., a predicate on control state + data values in \( \mathcal{M} \)).

- **Examples:** “counter \( \geq 1 \)”

  “the control-state is \( q \) or else \( c = 0 \)”

• We can label state \( s \) with the propositions \( L(s) \subseteq P \) that hold true at \( s \). Thus, \( L: S \mapsto \Sigma_P \), where the alphabet \( \Sigma_P = 2^P \) is the set of subsets of \( P \).

• We thus get a labeled transition system (**Kripke structure**) \( \mathcal{K}_P = (S, \Sigma_P, R, L, \text{Init}) \)
associating an $\omega$-language with an EFSM

- For a run $\pi = s_0 \rightarrow s_1 \rightarrow s_2 \ldots \ldots \text{ of } K_P$, we let

$$L(\pi) \triangleq L(s_0) L(s_1) \ldots \in (\Sigma_P)^\omega$$

be the ω-word associated with the run.

- We define $L_P(M) \triangleq L(K_P) \triangleq \{ L(\pi) \mid \pi \text{ is a run of } K_P \}$ to be the ω-language associated with $M$

(with respect to proposition set $P$).
assumptions about $\mathcal{K}$ for our algorithms & their analysis

- We assume $S$ is at most countable, and hence each $s \in S$ has a finite description. (For EFSMs, $S$ is finite.)
- For each $s \in S$,
  - we assume there are a finite set of successor states $\text{Succ}(s) = \{s' \mid (s,s') \in R\}$, and that you can “efficiently” compute them.
  - Thus, Guards & Actions are assumed to be “efficiently” computable.
  - We also assume each proposition $p_i \in P$ (a predicate on states $s$) is “efficiently” computable.
A quick detour: Büchi automata

- If we add a set $F \subseteq S$ of “accepting” states to a Kripke structure we get a **Büchi automaton**:

  $$\mathcal{A} = (S, \Sigma, R, L, \text{Init}, F)$$

- A run $\pi = s_0 \rightarrow s_1 \rightarrow s_2 \rightarrow \ldots$ of $\mathcal{A}$ is now called an **accepting** run iff for *infinitely many* $i \in \mathbb{N}$, $s_i \in F$.

- We’ll allow a set of initial states $\text{Init} \subseteq S$, rather than just one. A run $\pi$ must now have $s_0 \in \text{Init}$.

- We associate with $\mathcal{A}$ the $\omega$-language

  $$L(\mathcal{A}) = \{ L(\pi) \mid \pi \text{ is an accepting run of } \mathcal{A} \}.$$ 

- We say $\omega$-word $w \in \Sigma^\omega$ is **accepted** by $\mathcal{A}$ if $w \in L(\mathcal{A})$.

- In a **generalized** Büchi automaton, instead of one accepting set $F$, we have a family $\mathcal{F} = \{ F_1, \ldots, F_d \}$, and $\pi$ is accepting iff for all $F \in \mathcal{F}$, for infinitely many $i \in \mathbb{N}$, $s_i \in F$. 
specification of properties: temporal logic

• What is temporal logic?
  - It is a language for describing relationships between the occurrence of events over time.
  - It comes is several flavors, e.g., Linear vs. Branching time. We focus on (propositional) **Linear Temporal Logic (LTL)**.

- **Example 1:**
  “Until event stop occurs, every occurrence of event request is eventually followed by an occurrence of event response”:

  \[(\text{request } \rightarrow \text{"eventually" response}) \text{ "Until" stop}\]

  In LTL syntax:

  \[(\text{request } \rightarrow \lozenge \text{ response}) \text{ U stop}\]

- **Example 2:**
  “infinitely often pulse”: “always” “eventually” pulse

  In LTL syntax:

  \[\square \lozenge \text{ pulse}\]
Why use temporal logic to specify properties?

- [Pnueli’77] and others recognized that correctness assertions for *reactive* systems are best phrased in terms of occurrence of events during the entire, potentially indefinite, execution of the system. Not just what it outputs when it halts.

- Indeed, systems like the *Windows OS* aren’t really supposed to “halt and produce output”. Rather, they should forever *react* to stimuli from their environment in a “*correct*” manner.
• There exist properties that we might wish to express that aren’t expressible in LTL, nor in other temporal logics.
• But we need to balance expressiveness of the specification language with algorithmic efficiency of checking such properties.
• temporal logics, and the related formalism of $\omega$-automata, provide a reasonable balance.
• more on LTL vs. branching-time logics later.....
syntax of LTL

- LTL formulas (with base propositions $P$) are built from:
  - atomic propositions: $P = \{p_1, \ldots, p_k\}$,
  - boolean connectives: $\{\neg, \lor, \land\}$,
  - temporal operators: $\{\Diamond, \lozenge, \bigcirc, U, V\}$.
  (we also use parentheses to disambiguate formulas)

- Inductively, the set of LTL formulas are:
  - $p_i$, for all $p_i \in P$.
  - if $\psi_1$ and $\psi_2$ are LTL formulas, then so are
    $\neg\psi_1$, $\psi_1 \lor \psi_2$, $\bigcirc \psi_1$, and $\psi_1 U \psi_2$.

- The other connectives & operators are expressively redundant:
  \begin{align*}
  \psi_1 V \psi_2 & \equiv \neg (\neg \psi_1 U \neg \psi_2), \\
  \Diamond \psi & \equiv \text{true} U \psi, \quad \text{(where true} \equiv p_1 \lor \neg p_1) \\
  \Box \psi & \equiv \neg \Diamond \neg \psi
  \end{align*}
semantics of LTL

• We interpret a formula $\psi$ as expressing a property of $\omega$-words, i.e., an $\omega$-language $L(\psi) \subseteq (\Sigma^P)^\omega$.

• For $\omega$-word $w = w_0w_1w_2 \ldots \in (\Sigma^P)^\omega$, let $w[i] = w_iw_{i+1}w_{i+2} \ldots$ be the suffix of $w$ starting at position $i$. We define the “satisfies” relation, $\models$, inductively:
  - $w \models p_j$ iff $p_j \in w_0$ (for any $p_j \in P$).
  - $w \models \neg \psi$ iff $w \not\models \psi$.
  - $w \models \psi_1 \lor \psi_2$ iff $w \models \psi_1$ or $w \models \psi_2$.
  - $w \models \Diamond \psi$ iff $w[1] \models \psi$.
  - $w \models \psi_1 \lor \psi_2$ iff $\exists i \geq 0$ such that $w[i] \models \psi_2$, 
    & $\forall j, 0 \leq j < i, w[j] \models \psi_1$.

• Let $L(\psi) = \{ w \mid w \models \psi \}$.
Extending our definition of “satisfies” to transition systems, and EFSMs, we say:

• $\mathcal{K} \models \psi$ iff for all runs $\pi$ of $\mathcal{K}_p$, $L(\pi) \models \psi$,
  i.e., $L(\mathcal{K}_p) \subseteq L(\psi)$.

• $\mathcal{M} \models \psi$ iff $\mathcal{K} \models \psi$ (where $\mathcal{K}$ is the underlying transition system of $\mathcal{M}$.)
a few words about branching-time TL’s

- Branching-time (BT) temporal logics allow us to speak about many executions of $M$ at the same time, using quantification over paths $\pi$.

- **Example**: “In all executions, at all times there is a way to reach the ‘reset’ state”, would in the branching-time logic CTL be expressed as:

  $$A \Box E \Diamond \text{reset}$$

- Thus, unlike LTL, we interpret BT logics not on $\omega$-words, but on $\omega$-trees, consisting of all runs rooted at the initial state, labeled by $\Sigma_p$. 
linear vs. branching-time logics

some advantages of LTL

• LTL properties are preserved under “abstraction”: i.e., if $M$ “approximates” a more complex model $M'$, by introducing more paths, then
  \[ M \models \psi \Rightarrow M' \models \psi \]

• “counterexamples” for LTL are simpler: consisting of single executions (rather than trees).

• The automata-theoretic approach to LTL model checking is simpler (no tree automata involved).

• Anecdotally, it seems most properties people are interested in are linear-time properties.

some advantages of BT logics

• BT allows expression of some useful properties like ‘reset’.

• CTL, a limited fragment of the more complete BT logic CTL*, can be model checked in time linear in the formula size (as well as in the transition system). But formulas are usually far smaller than system models, so this isn’t as important as it may first seem.

• Some BT logics, like $\mu$-calculus and CTL, are well-suited for the kind of fixed-point computation scheme used in symbolic model checking.

My bias: linear: LTL and more generally $\omega$-regular properties on words.
The automata-theoretic approach to LTL model checking

[Vardi-Wolper’86]:
(informed by much prior work: [C-E’81, Q-S’81, L-P’85, K’85, …])

system model $\mathcal{M}$

LTL property $\psi$

model checker

Check that $\mathcal{M} \vDash \psi$
by checking that
$L(\mathcal{K}_p) \cap L(\mathcal{A} \neg \psi) = \emptyset$

Convert $\neg \psi$ to
Büchi automaton $\mathcal{A} \neg \psi$, so that
$L(\neg \psi) = L(\mathcal{A} \neg \psi)$

Yes!

No! +
“counterexample”
why does this scheme work?

\[ M \models \psi \iff L_P(M) \subseteq L(\psi) \]

\[ \iff L(K_P) \subseteq L(\psi) \]

\[ \iff L(K_P) \cap (\Sigma_p^\omega \setminus L(\psi)) = \emptyset \]

\[ \iff L(K_P) \cap L(\neg \psi) = \emptyset \]

\[ \iff L(K_P) \cap L(A \neg \psi) = \emptyset . \]
our algorithmic tasks

We have reduced LTL model checking to two tasks:

1. Convert an LTL formula $\varphi$ ($= \neg \psi$) to a Büchi automaton $A_\varphi$, such that $L(\varphi) = L(A_\varphi)$.
   - Can we in general do this? yes......

2. Check that $\mathcal{M} \models \psi$, by checking that the intersection of languages $L(\mathcal{K}_p) \cap L(A_{\neg \psi})$ is empty.
   - It would be unwise to first construct all of $\mathcal{K}_p$ from $\mathcal{M}$, because $\mathcal{K}$ can be far too big (state explosion).
   - Instead, we shall see how to construct states of $\mathcal{K}_p$ only as needed.
Translating LTL to Büchi automata: basic ideas

• First, let’s put LTL formulas $\varphi$ in **normal form** where:
  - $\neg$‘s have been “pushed in”, applying only to propositions.
  - the only temporal operators are $U$ and its dual, $V$.

- States of $\mathcal{A} \varphi$ will be **sets of subformulas** of $\varphi$, thus if we have $\varphi = p_1 U \neg p_2$, a state is given by $\Phi \subseteq \{ p_1, \neg p_2, p_1 U \neg p_2 \}$.

- Consider a word $w = w_0 w_1 w_2 \ldots \ldots$, such that $w \models \varphi$, where, e.g., $\varphi = \psi_1 U \psi_2$.

- Mark each position $i$ with the set of subformulas $\Phi_i$ of $\varphi$ that hold true there:

\[
\begin{align*}
\Phi_0 & \Phi_1 \Phi_2 \ldots \ldots \\
w_0 & w_1 w_2 \ldots \ldots
\end{align*}
\]

- Clearly, $\varphi \in \Phi_0$. But then, by consistency, either:
  - $\psi_1 \in \Phi_0$, and $\varphi \in \Phi_1$, or
  - $\psi_2 \in \Phi_0$.

- Such consistency rules will dictate our states & transitions ....
The translation: LTL to (generalized) Büchi

Let \( \text{sub}(\varphi) \) denote the set of subformulas of \( \varphi \).
We define \( A_{\varphi} = (Q, \Sigma, R, L, \text{Init}, F) \) as follows.

First, the state set:
- \( Q = \{ \Phi \subseteq \text{sub}(\varphi) \mid \text{s.t. } \Phi \text{ is internally consistent} \} \).
  - For \( \Phi \) to be internally consistent we should, e.g., have:
    - if \( \psi \lor \gamma \in \Phi \), then \( \psi \in \Phi \) or \( \gamma \in \Phi \).
    - if \( \psi \land \gamma \in \Phi \), then \( \psi \in \Phi \) and \( \gamma \in \Phi \).
    - if \( p \in \Phi \) then \( \neg p \notin \Phi \), & if \( \neg p \in \Phi \) then \( p \notin \Phi \).
    - if \( \psi \cup \gamma \in \Phi \), then (\( \psi \in \Phi \) or \( \gamma \in \Phi \)).
    - if \( \psi \lor \gamma \in \Phi \), then \( \gamma \in \Phi \).
  - (some of these are not strictly necessary, but we get a smaller automaton by using them to eliminate redundant states).
the translation continued....... 

Now, labeling the states of \( \mathcal{A}_\varphi \):

- The alphabet is \( \Sigma = 2^{P \cup \neg P} \), where \( \neg P = \{ \neg p \mid p \in P \} \).
- The labeling, \( L: Q \rightarrow \Sigma \), is \( L(\Phi) = \{ l \in P \cup \neg P \mid l \in \Phi \} \).
- **Note:** \( \sigma = \{ l_1, ..., l_m \} \in \Sigma \) denotes a boolean term (a conjunct of literals) \( l_1 \land ... \land l_m \), not one symbol of \( \Sigma_p = 2^P \).

  - Now, a word \( w = w_0w_1 ... \in (\Sigma_p)^\omega \) is in \( L(\mathcal{A}_\varphi) \) iff there’s a run \( \pi = \Phi_0 \rightarrow \Phi_1 \rightarrow \Phi_2 ... \) of \( \mathcal{A}_\varphi \), s.t., \( \forall i \in \mathbb{N}, w_i \) “satisfies” \( L(\Phi_i) \), i.e., is a “satisfying assignment” for the term \( L(\Phi_i) \).

  - This constitutes a slight redefinition of Büchi automata, but it is important for facilitating a much more compact \( \mathcal{A}_\varphi \).
translation continued....

Now, the transition relation, and the rest of $A\phi$

- $R \subseteq Q \times Q$, where $(\Phi, \Phi') \in R$ iff:
  - if $\bigcirc \psi \in \Phi$, then $\psi \in \Phi'$.
  - if $\psi \cup \gamma \in \Phi$ then $\gamma \in \Phi$, or ($\psi \in \Phi$ and $\psi \cup \gamma \in \Phi'$).
  - if $\psi \forall \gamma \in \Phi$ then $\gamma \in \Phi$, and $\psi \in \Phi$ or $\psi \forall \gamma \in \Phi'$.

- $\text{Init} = \{ \Phi \in Q \mid \varphi \in \Phi \}$.

- For each $\chi \in \text{sub}(\varphi)$ of the form $\chi = \psi \cup \gamma$, there is a set $F_{\chi} \in \mathcal{F}$, such that $F_{\chi} = \{ \Phi \in Q \mid \chi \notin \Phi \text{ or } \gamma \in \Phi \}$.

**Lemma:** $L(\varphi) = L(A\varphi)$.

but, at this point $A\varphi$ is a generalized Büchi automaton.....
From generalized-Büchi automata to Büchi automata

From $\mathcal{A} = (Q, \Sigma, R, L, \text{Init}, \mathcal{F} = \{F_0, \ldots, F_d\})$, we construct $\mathcal{A}' = (Q', \Sigma, R', L', \text{Init}', F')$:

- $Q' = Q \times \{0, \ldots, d\}$.
- $R' \subseteq Q' \times Q'$, where $((q,i),(s,j)) \in R'$ iff $(q,s) \in R$, and either $q \notin F_i$ and $i=j$, or $q \in F_i$ and $j = (i+1) \mod (d+1)$.
- $L'((q,i)) = L(q)$.
- $\text{Init}' = \{ (q,0) \mid q \in \text{Init} \}$.
- $F' = \{ (q,0) \mid q \in F_0 \}$.

**Lemma:** $L(\mathcal{A}') = L(\mathcal{A})$. 
We have shown:

**THEOREM:** Every LTL formula $\varphi$ can be converted to a Büchi automaton $A_\varphi$, such that $L(\varphi)=L(A_\varphi)$. Furthermore, $|A_\varphi| \leq 2^{O(|\varphi|)}$.

We need $A_\varphi$ to be as small as possible. But it is PSPACE-hard to find an optimal $A_\varphi$.

- [GPVW’95] give a more efficient algorithm, constructing only states as needed, in practice yielding smaller automata. Related translations based on alternating-automata, [MSS’88, V’94, GO’01], yield further improvements.

- Beyond optimizing the translation itself, key optimizations are:
  - [EH’00, SB’00]: “massage” LTL formulas before translation, using “proof-theoretic” rewrite rules.
  - [EH’00, SB’00, EWS’01, E’02]: make Büchi automata smaller after translation, using various “fair” simulation quotients.

- In the worst case, exponential blowup is unavoidable. Consider:

  $\Diamond p_1 \land \Diamond p_2 \land \ldots \land \Diamond p_n$
Can we go in the other direction? i.e., can every Büchi automaton $\mathcal{A}$ be converted to an LTL formula $\psi_\mathcal{A}$, such that $L(\mathcal{A}) = L(\psi_\mathcal{A})$?

• No, not quite. But there are relatively simple modifications to LTL ([Wolper’83]) that make it as expressive as Büchi automata.

• For example, LTL formulas prefixed by quantification of one proposition, $\exists p \psi$, suffice to express all $\omega$-regular languages ([T’82,E’00]).

What about $\omega$-automata with other acceptance criteria? Rabin, Streett, Muller, .................
what’s with all these names?

- Each such acceptance condition simply defines a different family of boolean formulas over predicates $\text{inf}(q)$ meaning “state $q$ occurs infinitely often in the run”.

<table>
<thead>
<tr>
<th>Person’s name</th>
<th>Class of acceptance formulas</th>
</tr>
</thead>
<tbody>
<tr>
<td>Büchi</td>
<td>$\bigvee_{q \in F} \text{inf}(q)$</td>
</tr>
<tr>
<td>(generalized) Büchi</td>
<td>$\bigwedge_{F \in \mathcal{F}} \bigvee_{q \in F} \text{inf}(q)$</td>
</tr>
<tr>
<td>Rabin</td>
<td>$\bigvee_{i \in [k]} (\neg (\bigvee_{q \in L_i} \text{inf}(q)) \land (\bigvee_{i \in R_i} \text{inf}(q)))$</td>
</tr>
<tr>
<td>Streett</td>
<td>$\bigwedge_{i \in [k]} (\bigvee_{q \in L_i} \text{inf}(q)) \rightarrow (\bigvee_{i \in R_i} \text{inf}(q))$</td>
</tr>
<tr>
<td>Muller</td>
<td>truth table shorthand for arbitrary boolean formulas</td>
</tr>
<tr>
<td>your name here</td>
<td>your favorite formulas (with desirable properties)</td>
</tr>
</tbody>
</table>

- **Theorem:** all of these can be converted to equivalent (non-deterministic) Büchi automata. (but translation costs vary.)
step 2: determining whether \( L(K_p) \cap L(A\phi) = \emptyset \):

first, the basic product construction

Given \( K_p = (S,R,L, \text{init}) \) and \( A\phi = (Q,\Sigma_p,R',L',\text{Init}',F') \),
we define the “product” Büchi automaton:

\[
K_p \otimes A\phi = (S^\otimes, \Sigma_p, R^\otimes, L^\otimes, \text{Init}^\otimes, F^\otimes)
\]

\[
S^\otimes := \{ (s,q) \in S \times Q \mid \text{L(s) satisfies L'(q)} \} ,
\]

(\text{recall: L'(q) is a term})

\[
R^\otimes := \{ ((s,q),(s',q')) \in S^\otimes \times S^\otimes \mid (s,s') \in R \land (q,q') \in R' \} ,
\]

\[
L^\otimes((s,q)) := \text{L(s)} ,
\]

\[
\text{Init}^\otimes := \{ (\text{init},q) \in S^\otimes \mid q \in \text{Init}' \} ,
\]

\[
F^\otimes := \{ (s,q) \in S^\otimes \mid q \in F' \} .
\]

Fact 1: \( L(K_p \otimes A\phi) = L(K_p) \cap L(A\phi) \).

But how do we determine whether \( L(K_p \otimes A\phi) = \emptyset \)?
determining whether $L(K_p) \cap L(A\phi) = \emptyset$, continued......

**Fact 2:** $L(K_p \otimes A\phi) = \emptyset$ iff there is no reachable cycle in the Büchi automaton $K_p \otimes A\phi$ containing a state in $F^\otimes$.

So, it appears we are left only with the task of finding whether there is such a reachable cycle.

**But, NOTE:** we are not given $K_p$. We’re given $M$! We won’t first build $K_p$ from $M$, because $K_p$ can be HUGE!!
on-the-fly bad cycle detection

- Given $\mathcal{M}$ and $A\phi$, we will explore the state space of $K_p \otimes A\phi$, but only constructing states as we need them. If we find a bad cycle, we stop before exploring all of $K_p \otimes A\phi$.

- We begin with the set of initial states $\text{Init}^\otimes$, which we can obtain directly from $\mathcal{M}$ and $A\phi$.

- For $s \in S^\otimes$, by assumption we can compute $\text{Succ}^\otimes(s)$ efficiently, so we can do Depth First Search, to find all reachable states of $K_p \otimes A\phi$.

- But how do we find bad cycles efficiently?
  - We could simply compute the SCCs of $K_p \otimes A\phi$ using the standard DFS algorithm, and check if $\exists$ a reachable (nontrivial) SCC containing a state of $F^\otimes$.
  - But this is too inefficient in practice. We will use a cool nested DFS [CVWY’90].
The [CourcobetisVardiWolperYannakakis’90] algorithm

Input: $\mathcal{M}$ and $A_\varphi$,
Initialize: Stack$1:=\emptyset$, Stack$2:=\emptyset$,
Table$1:=\emptyset$, Table$2:=\emptyset$;

procedure Main() {
  foreach $s \in \text{Init}^*$
    { if $s \not\in \text{Table}1$ then DFS$1(s)$; }
  output("no bad cycle");
  exit;
}

procedure DFS$1(s)$ {
  push(s,Stack$1$);
  hash(s,Table$1$);
  foreach $t \in \text{Succ}^*(s)$
    { if $t \not\in \text{Table}1$ then DFS$1(t)$; }
  if $s \in F^*$ then { DFS$2(s)$; }
  pop(Stack$1$);
}

procedure DFS$2(s)$ {
  push(s,Stack$2$);
  hash(s,Table$2$);
  foreach $t \in \text{Succ}^*(s)$ do {
    if $t \not\in \text{Table}2$ then
      DFS$2(t)$
    else if $t$ is on Stack$1$ {
      output("bad cycle:");
      output(Stack$1$,Stack$2$,t);
      exit;
    }
  }
  pop(Stack$2$);
}

/*note: upon finding a bad cycle, Stack$1$,Stack$2$,+t, determines a counterexample: a bad cycle reached from an init state.*/
**Theorem:** The [CVWY’90] algorithm outputs “no bad cycle” iff \( L(K_P) \cap L(A_\varphi) = \emptyset \). If it outputs “bad cycle:....”, then content of Stack1 + Stack2 + t defines a (looping) execution of \( M \) that does not satisfy property \( \varphi \).

The algorithm (properly implemented) runs in (worst-case) time and space: \( O(|K_P| \times |A_\varphi|) \), i.e., \( O(2^{O(|M|)} \times |A_\varphi|) \).

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- If we find a bad cycle, we can get lucky and find it much faster than the worst-case time.
- The crux of the *state explosion problem* is the \( 2^{O(|M|)} \) factor.
- Note that we are better off having \( A_\varphi \) as small as possible before running this check.
methods for mitigating the state explosion problem

We’ll look briefly at:

- **Bit-state hashing.**
- **Partial-order reduction.**

We will not look at:

- **Symbolic model checking:** See A. Cimatti’s lecture.
  idea: view $R$ compactly as a boolean formula $R(b_1,\ldots,b_n,b'_1,\ldots,b'_n)$, represented & manipulated as an OBDD.

- **Abstraction & Compositional model checking:**
  See M. Huth’s & L. de Alfaro’s lectures, respectively.

- **Symmetry considerations:**
  idea: often, a system consists of related or identical components. Exploit non-trivial symmetries in state space to restrict the search space. (See, e.g., the book [CGP’00].)

- **SAT-based bounded model checking:** (see [BCCZ’99],……)
  idea: construct a boolean formula that is satisfiable iff “there is a counterexample to $M \models \varphi$ of ‘length’ at most $k$”, and check its satisfiability using a SAT-solver.
bit-state hashing [H’88]

- Ordinarily, collisions in hash tables (used, e.g., in the [CVWY’90] algorithm) are resolved by, e.g., chaining or open addressing.
- In practice, this has a high cost.
- “Bit-state hashing” reduces this cost by having each location of the hash table contain only one bit. If any state hashes to that location, the bit is set to 1.
- Thus, because of collisions, the algorithm might over-approximate what states it thinks have already been processed.
- Hence, some of the state space might not be searched, and errors (bad cycles) might be missed.
- But experiments suggest that variations on this scheme ([H’88,GH’93]) perform reasonably well in many circumstances.
In an asynchronous concurrent system $M = \text{Proc}_1 \parallel \ldots \parallel \text{Proc}_n$, with control states $Q_1 \times \ldots \times Q_n$, sometimes actions occurring on different processes are independent, and can be commuted without changing any relevant fact about the execution.

Example: if $\text{Proc}_1$ and $\text{Proc}_2$ each increment a local counter, we might not care in what order this happens.

The idea of partial-order reduction is to group together executions that differ merely by commutations of independent transitions, and try to confine the search to only explore one representative from each such “equivalence class”.

There are several techniques for choosing a subset of transitions during, e.g., nested DFS search, so that these transitions will “cover” all equivalence classes of executions, with hopefully little redundancy.

There are many details involved. See the above references and [CGP’00].
review

We have:

• defined EFSMs as system models, and defined their underlying transition systems.

• defined (linear) temporal logic (LTL) as a specification formalism.

• defined Büchi automata.

• provided an algorithm for model checking an EFSM against an LTL specification, using:
  - an algorithm to translate an LTL formula to a Büchi automaton.
  - an “on-the-fly” algorithm to determine if the intersection of the language associated with an EFSM and that of a Büchi automaton is empty.

• discussed some approaches to coping with state explosion.
( I rue the day I decided to use PowerPoint for this. )